

4BLE1		2024-2025			Semester I	
Time/Day	Monday	Tuesday	Wednesday	Thursday	Friday	
9:00 am						
10:00 am		EE451 System on Chip Design I (ENG 3001)	EE450 Power Systems (SC005, Tyndall theatre, Arts/Science Bldg)	EE450 Power Systems (ENG G017)		
11:00 am	EE4101 Mobile Networks: Architecture and Services (ENG 2001)	EE4101 Mobile Networks: Architecture & Services (ENG3001)	EE4101 Mobile networks: Architecture and Services (ENG 2001)	EE Lab (ENG 3001/3002)		
12:00 pm	EE4104 Machine Learning and Artificial Intelligence Application (ENG2001)			EE Lab (ENG 3001/3002)	EE4104 Machine Learning and Artificial Intelligence Application (CSB G005)	
1:00 pm		EE445 Digital Signal Processing (ENG 2002)				
2:00 pm	EE347 Embedded Systems Applications Programming (ENG 2033)	EE4104 Machine Learning and Artificial Intelligence Application (ENG G047)		EE451 System on Chip Design I (ENG 3001)		
3:00 pm	EE445 Digital Signal Processing (ENG 2002)		EE347 Embedded Systems Appls Programming (ENG 3001)	EE451 System on Chip Design I (ENG 3001)		
4:00 pm	EE445 Digital Signal Processing (ENG 2002)	EE450 Power Systems (ENG G017)	EE347 Embedded Systems Appls Programming (ENG 3001)			
5:00 pm			EE347 Embedded Systems Appls Programming (ENG 3001)			
6:00 pm						

Electrical & Electronic Engineering

Please check Canvas regularly for details of individual modules . All timetable slots are of one hour duration unless otherwise specified

Date

September 19, 2024

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